

# *Design and Investigation of DC and Microwave Characteristics of InGaP/InGaAs/GaAs Dual Channel Pseudomorphic HEMT (DC-PHEMT)*

Limali Sahoo, Meryleen Mohapatra and A.K. Panda

**Abstract**— A InGaP/InGaAs/GaAs dual channel pseudomorphic HEMT (DC- PHEMT) with interesting triple doped sheets having gate length 800nm is modeled and simulated by using 2-dimensional simulation package ATLAS from Silvaco. Different DC and microwave performances of the above device are analyzed and investigated to judge the potential of the device for high- performance digital device applications. Due to the schottky behavior of InGaP, good pinch-off and saturation characteristics, high drain saturation current, large and linear transconductance and excellent microwave characteristics are obtained. The studied dual channel HEMT with delta-doped sheet densities and proper layers; prove promise for electronic applications.

**Keywords**— Dual channel, Pseudomorphic HEMT,  $\delta$ -doped sheet, schottky behaviour, InGaP.

## I. Introduction

Due to the rapid progress in fabrication technology, many compound semiconductor devices such as light-emitting diodes (LEDs), laser diodes (LDs), switching devices, field effect transistors (FETs), hetero-junction bipolar transistors (HBTs) and high electron mobility transistors (HEMTs) have been extensively adopted [1-3]. Major limitation of silicon MOSFETs is that silicon itself is an inherently low mobility material. Most compound materials have significantly higher mobility than silicon; hence devices made using compound semiconductors can exhibit higher frequencies of operation. Modulation doping provides an important advantage which is, the free carrier concentration within the semiconductor layer can be increased significantly without the introduction of dopant impurities.

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In case of doped channel FETs the presence of un-doped wide band-gap layer between the gate metal and channel can improve the turn-on voltage, breakdown voltage and linearity [4]. But the high impurity scattering effect degrades the electron mobility. So the device is restricted for high frequency operation. There-fore, because of high electron mobility, low power consumption, higher unity current gain frequency, high break-down and high power characteristics; HEMTs have attracted much attention [5,6].

Among compound semiconductor materials; InGaP, InGaAs and its related materials have superior properties that make them promising candidates for low noise, low power and high speed electronic applications. InGaAs has small band gap, small electron effective mass, hence high electron mobility (can be 15000 cm<sup>2</sup>/Vs) and high electron peak velocity which makes it a great choice for channel layer [7,8]. The high Indium content in InGaAs channel can offer high electron mobility and peak velocity [9,10]. Such InGaP/InGaAs/GaAs HEMT have been identified as one of the most promising device for high speed microwave applications. The applications include advanced communication, radar, passive imaging and remote sensing. Thus, this work focuses on the systematic study of DC and RF performances of InGaP/InGaAs/GaAs dual channel Pseudomorphic HEMT.

## II. Device Structure

The Schematic cross section and different layers of studied DCPHEMT is shown in fig 1. Structure of studied device is developed on a 5000Å semi-insulating (S.I) GaAs substrate. It consists of a 450 Å i- In<sub>0.49</sub>Ga<sub>0.51</sub>P buffer layer, a 30Å  $\delta$ -doped sheet 3 with the doping density of  $\delta_3(n^+)$ , a 50 Å i- In<sub>0.49</sub>Ga<sub>0.51</sub>P spacer layer, a 100 Å i- In<sub>0.2</sub>Ga<sub>0.8</sub>As channel layer, a 40 Å i-GaAs spacer layer, a 30 Å  $\delta$ -doped sheet 2 with the doping density of  $\delta_2(n^+)$ , a 40 Å i-GaAs spacer layer, a 100 Å i- In<sub>0.2</sub>Ga<sub>0.8</sub>As channel layer, a 50 Å i-- In<sub>0.49</sub>Ga<sub>0.51</sub>P spacer layer, a 30 Å  $\delta$ -doped sheet 1 with the doping density of  $\delta_1(n^+)$ , a 160 Å In<sub>0.49</sub>Ga<sub>0.51</sub>P Schottky barrier layer and a 300 Å GaAs cap layer.

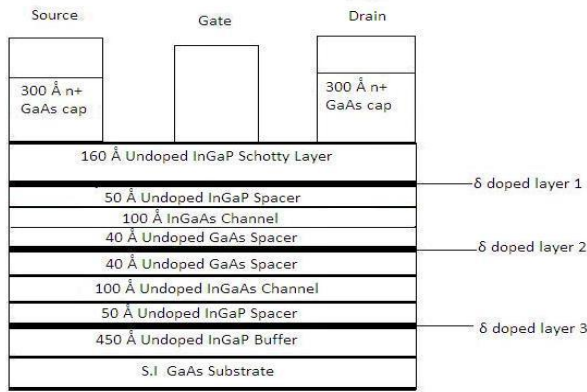


Figure 1. Schematic cross section of the studied dual channel pseudomorphic HEMT

The use of  $\delta$ -doping layers is to provide high electron densities in the channel and therefore results in high transconductances, current densities and cut-off frequencies. The use of Spacer layer is to reduce impurity scattering and hence enhancing electron mobility. The  $n^+$ -doped Cap layer minimizes the contact resistance of the source and drain contacts that provides protection from oxidation. Schottky-Barrier layer provides a Schottky contact between gate-metal and semiconductor material. As a result, excellent device properties with low leakage current through the leakage path and provide good carrier confinement in the InGaAs channel. In this paper, a two-dimensional simulator ATLAS is used to analyze the structure and to get DC and microwave characteristics. The different material parameters used in the simulation are stated in Table 1. The device is simulated by taking the gate length 800nm.

Table 1.

Physical parameters used for the simulation.

Parameter	Symbol	Value
In <sub>0.49</sub> Ga <sub>0.51</sub> P energy gap (ev)	E <sub>g</sub>	1.92
In <sub>0.2</sub> Ga <sub>0.8</sub> As energy gap (ev)	E <sub>g</sub>	0.75
GaAs energy gap(ev)	E <sub>g</sub>	1.42
Saturation velocity (cm/s) In <sub>0.49</sub> Ga <sub>0.51</sub> P	$\gamma$	$1.0 \times 10^6$
Saturation velocity (cm/s) In <sub>0.2</sub> Ga <sub>0.8</sub> As	$\gamma$	$7.7 \times 10^6$
Saturation velocity (cm/s) GaAs	$\gamma$	$7.7 \times 10^6$
Low field mobility GaAs (cm <sup>2</sup> /Vs)	$\mu$	1000
Low field mobility InGaAs (cm <sup>2</sup> /Vs)	$\mu$	6000

### III. Result and Analysis

In this work three  $\delta$ -doped sheets as carrier supplier layers are employed. The doping density of  $\delta$ -doped sheets  $\delta_1(n^+)$ ,

$\delta_2(n^+)$ ,  $\delta_3(n^+)$  are  $10 \times 10^{12} \text{ cm}^{-2}$ ,  $5 \times 10^{12} \text{ cm}^{-2}$  and  $2 \times 10^{12} \text{ cm}^{-2}$  respectively. This causes linear characteristics of the DC-HEMT including transconductance and frequency response. On the other hand, the high doping with high carrier density is advantageous to drive the current. The un-doped wide band gap In<sub>0.49</sub>Ga<sub>0.51</sub>P material, used as the buffer and schottky layer reduces the leakage current. As a result good device performance is achieved.

#### A. Energy Band Diagram

The energy band diagram showing valence band and conduction band for DC HEMT structure is shown in fig-2. The band gap energy was found to be 1.4 eV for GaAs, 1.9 eV for InGaP and 1.1 eV for InGaAs which is same as that of the theoretical value.

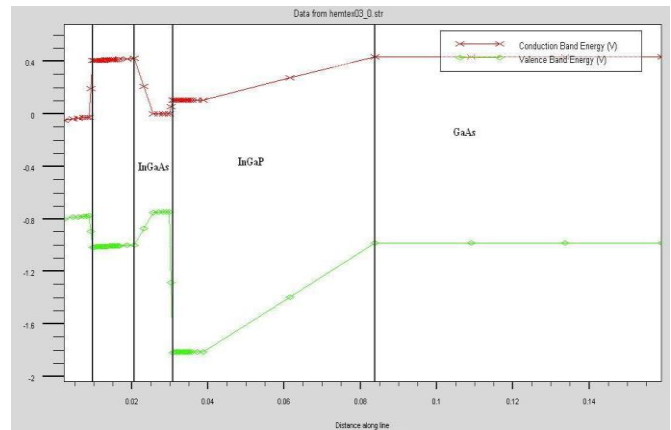


Figure 2. Energy Band Diagram of the DC HEMT

#### B. DC Characteristics

The DC current- voltage characteristics of the studied device are shown in fig. 3 and 4.

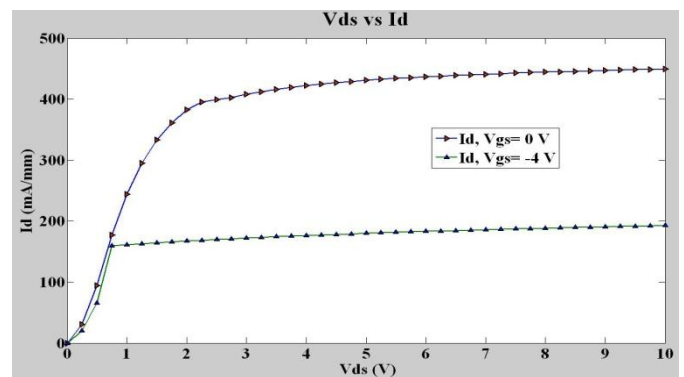


Figure 3. Drain characteristics of DC- PHEMT

Due to the suppression of leakage current, because of the presence of InGaP schottky and buffer layers, the device exhibits good pinch-off and saturation characteristics. The obtained drain current of the simulated device at  $V_{GS}=0\text{v}$  is

410mA/mm as shown in fig. 3. Fig.4 shows drain saturation current ( $I_{DS}$ ) and transconductance ( $g_m$ ) versus gate- source voltage  $V_{GS}$  at drain-source voltage fixed at  $V_{DS}=3.5V$ . The maximum value of transconductance and drain saturation current at  $V_{DS}= 3.5V$  are 210 ms/mm and 620mA/mm respectively.

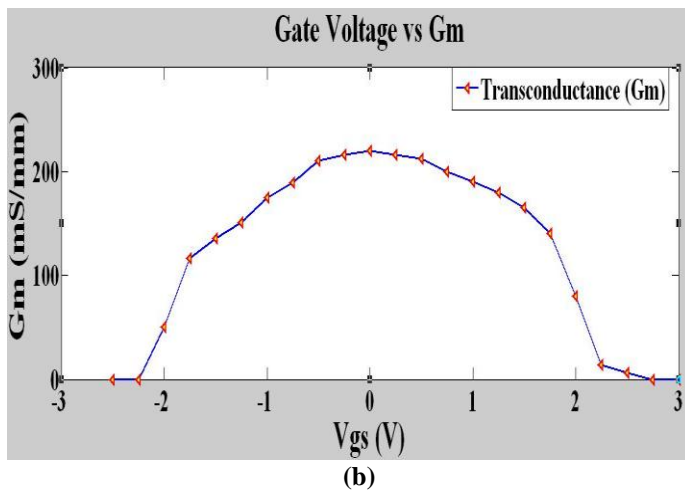
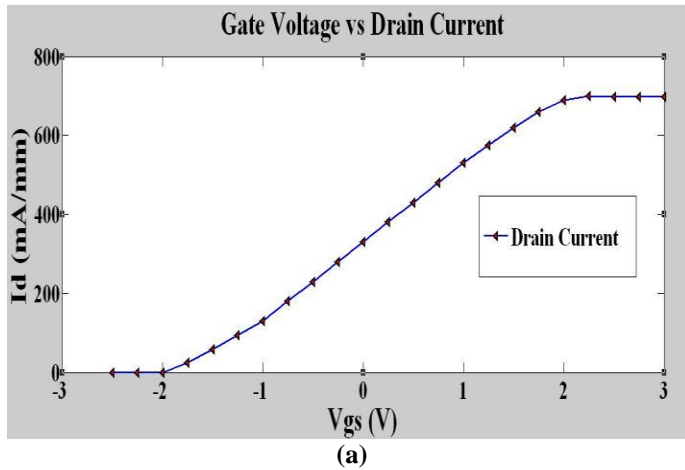


Figure 4. Drain saturation current and transconductance versus gate-source voltage at  $V_{DS}=3.5V$

### c. RF Performance

Fig.5. Shows the simulated response between frequency (Hz) and current gain (dB) from which unity gain cut off frequency  $f_T$  is calculated. The graph between frequency (Hz) and unilateral power gain (dB) is shown in fig. 6 from which maximum oscillation frequency  $f_{max}$  is obtained. The biased voltage is fixed at  $V_{DS}=3.5V$ . The tail region of the two simulated curve is extrapolated to 0dB which gives the value of  $f_T$  and  $f_{max}$ .

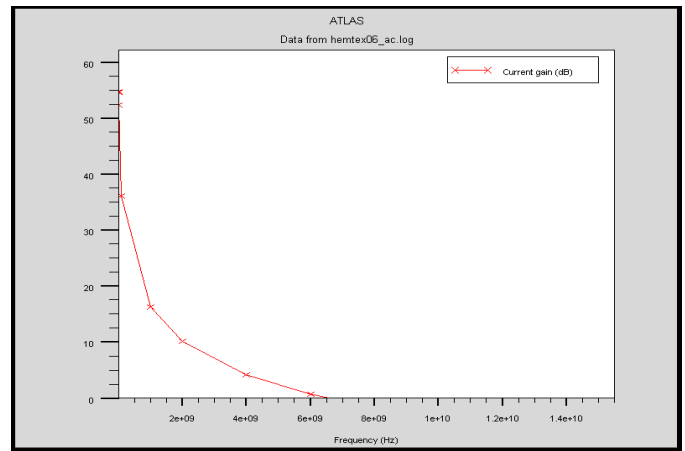


Figure 5. Frequency(Hz) vs current gain(dB)

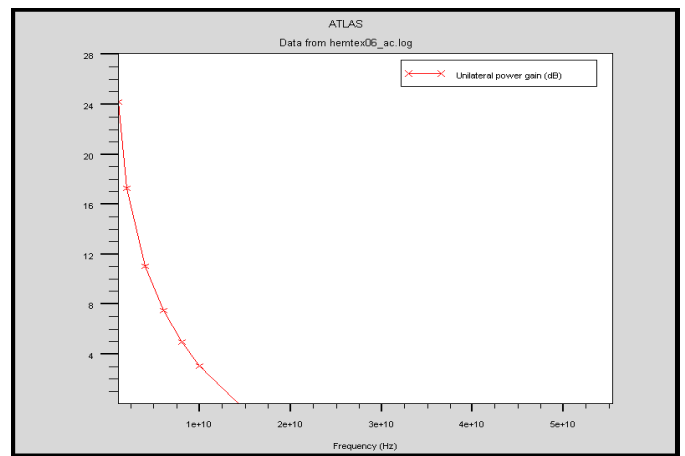


Figure 6. Frequency (Hz) vs Unilateral power gain(dB)

The measured  $f_T$  and  $f_{max}$  are 7 GHz and 15 GHz respectively at  $V_{DS}= 3.5V$  and  $V_{GS}=0V$ .

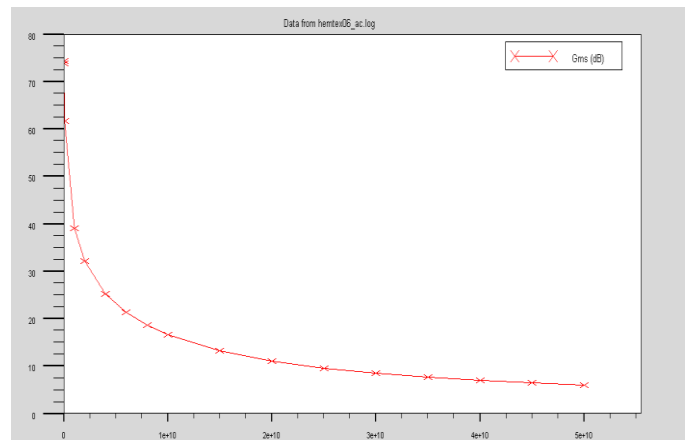


Figure 7. Frequency (GHz) vs Maximum stable gain  $G_{ms}$  (dB)

Fig. 7 shows the variation of maximum stable gain ( $G_{ms}$ ) in dB with frequency.  $G_{ms}$  of the device is defined as the highest

value of power-gain that is achieved by the device before the instability occurs. A steep decrease in  $G_{ms}$  is obtained for low frequencies up to around 10 GHz. After that, the variation of  $G_{ms}$  with frequency becomes constant upto 50 GHz. This indicates good stability performance of the device making it suitable for microwave and low-noise amplifier applications.

#### iv. Conclusion

The DC and microwave characteristics of InGaP/InGaAs/GaAs dual channel pseudomorphic HEMT with interesting  $\delta$ -doped sheets is successfully demonstrated by taking the gate length 800nm. A 2-dimensional simulation package ATLAS is used to study the device properties. High and linear transconductance, good saturation characteristics are obtained. It is observed that  $G_{ms}$  stays above 0 dB for frequencies up to 50GHz. This shows excellent stability performance of the device for low noise and high frequency applications. Thus this simulated device provides the promise for high performance microwave electronic applications

#### References

[1] C. Nguyen and M. Micovic, "The state-of-the-art of GaAs and InP power devices and amplifiers," *IEEE Trans. Electron Devices*, vol. 48, pp. 472-478, Mar. 2001.

[2] A. G. Baca, V. M. Hietala, D. Greenway, J. C. Zolper, D. F. Dubbert, I. R. Sloan, and R. J. Shul, "Self-aligned GaAs JFETs for low-power microwave amplifiers and RFICs at 2.4 GHz," *IEE Electron. Lett.*, vol. 35, pp. 308-309, 1999.

[3] Liu WC, Chang WL, Lour WS, Yu KH, Lin KW, Cheng CC, "Temperature dependence investigation of a high-performance inverted  $\delta$ -doped V-shaped GaInP/In<sub>x</sub>Ga<sub>1-x</sub>As/GaAs pseudomorphic high electron mobility transistor," *IEEE Trans Electron Dev* 2001.

[4] Y. S. Lin, S. S. Lu, and Y. J. Wang, "High-performance Ga<sub>0.51</sub>In<sub>0.49</sub>P/GaAs airbridge gate MISFET's grown by gas-source MBE," *IEEE Trans. Electron Devices*, vol. 46, pp. 921-929, June 1997.

[5] Lour WS, Tsai MK, Chen KC, Wu YW, Tan SW, Yang YJ, "Dual gate InGaP/InGaAs Pseudomorphic high electron mobility transistors with high linearity and variable gate-voltage swing," *Semicond Sci Technol* 2001.

[6] Yu KH, Chuang HM, Lin KW, Cheng SY, Cheng CC, Chen JY, "Improved temperature-dependent performances of a novel InGaP-InGaAs-GaAs double channel pseudomorphic high electron mobility transistor (DC-PHEMT)," *IEEE Trans Electron Dev* 2002.

[7] J. C. Huang, P. Saledas, J. Wendler, A. Platzker, W. Boulais, S. Shanfield, W. Hoke, P. Lyman, L. Aucoin, A. Miquelarena, C. Bedard, and D. Atwood, "A double-recessed Al<sub>0.24</sub>/GaAs/In<sub>0.16</sub>GaAs pseudomorphic HEMT for Ka- and Q-band power applications," *IEEE Electron Device Lett.*, vol. 14, pp. 456-458, Sept. 1993.

[8] L.W. Laih, S.Y. Cheng, W. C. Wang, P. H. Lin, J.Y. Chen, W. C. Liu, and W. Lin, "High-performance InGaP-InGaAs-GaAs step-compositioned doped-channel field-effect transistor (SCDCFET)," *IEE Electron. Lett.*, vol. 33, pp. 98-99, 1997.

[9] M. Meshkinpour, M. S. Goorsky, G. Chu, D. C. Streit, T. R. Block, and M. Wojtowicz, "Role of misfit dislocations on pseudomorphic high electron mobility transistors," *Appl. Phys. Lett.*, vol. 66, pp. 748-750, 1995.

[10] J. W. Matthews, "Defects associated with the accommodation of misfit between crystals," *J. Vac. Sci. Technol.*, vol. 12, pp. 126-133, 1975.



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